

MP0116



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Liou, Shiann-Ming

Group Art Unit: 2811

Serial No.: 10/051965

Examiner: Ori Nadav

Filed: January 16, 2002

Title: Semiconductor Device Power Distribution System and Method

#8  
Election  
+  
Amold A  
FJOES  
10-11-02

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence, and the documents attached hereto, are being deposited with the United States Postal Service as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231 on this date.

Date:

9/27/02

*Suzanne M. Forno*  
Suzanne M. Forno

**RESPONSE TO RESTRICTION REQUIREMENT  
AND  
CONDITIONAL PETITION FOR EXTENSION OF TIME (IF REQUIRED)**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Requirement for Restriction in the Office Action dated August 29, 2002, Applicant hereby elects Group I namely, Claims 1-16 for prosecution at this time. Applicant reserves the right to present the remaining claims in a divisional application at the appropriate time.

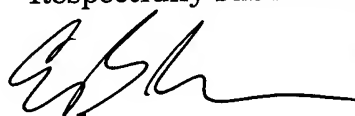
Accordingly, please cancel Claims 17-24 without prejudice or disclaimer of subject matter.

RECEIVED  
OCT 11 2002  
TECHNOLOGY CENTER 2800

U.S. Serial No. 10/051965

Applicant believes that no extension of time is required. However, this conditional petition is being made to provide for the possibility that Applicant inadvertently overlooked the need for a petition for extension of time. The Commissioner is hereby authorized to charge and credit deposit account 50-1236.

Respectfully submitted,

 9/27/00

Eric B. Janofsky  
Attorney for Applicant  
Registration No. 30759

Please address all correspondence to:

Marvell Semiconductor, Inc.  
Intellectual Property Department  
Mail Stop 509  
700 First Avenue  
Sunnyvale, CA 94085  
Phone: (408) 222-2500  
Fax: (408) 752-9034  
Customer No. 23624

Date: 9/27/00